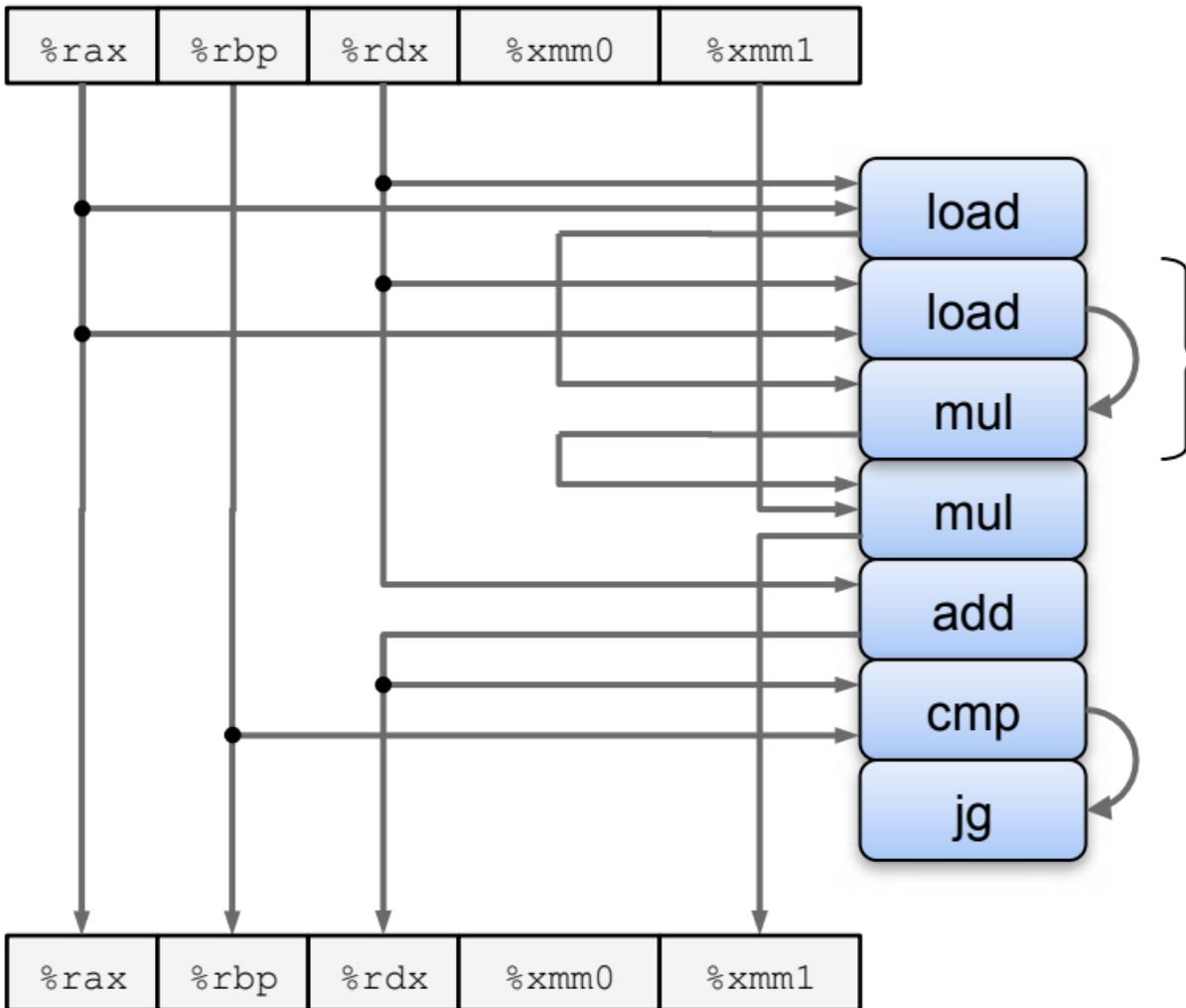


%rax	%rbp	%rdx	%xmm0	%xmm1
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%rax	%rbp	%rdx	%xmm0	%xmm1
------	------	------	-------	-------



vmovsd (%rax,%rdx,8), %xmm0

vmulsd 8(%rax,%rdx,8), %xmm0, %xmm0

vmulsd %xmm0, %xmm1, %xmm1

addq \$2,%rdx

cmpq %rdx,%rbp

jg loop